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debate however is the success, or lack thereof, of developing low power cost effective multi- standard multi-band chip-set solutions, particularly the radio part of the solution, catering for either or both technologies.

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Phase-Locked Loop (PLL) Fundamentals | Analog Devices

PLLs with Integrated VCOs for Microwave Market Applications. The HMC764LP6CE PLL with integrated VCO is optimized for narrow-band but high performance microwave communication

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A VCO is one of the key components in a PLL, and it has a great impact on the PLL's overall performance. For CMOS based VCO design in current technology, LC and Ring based VCOs are two typical choices used

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Design of CMOS Phase-Locked Loops: From Circuit Level to ...

As such, this book discusses design, modeling and optimization techniques for low power fully integrated broadband PLLs and VCOs in deep submicron CMOS. First, the PLL and VCO performances are studied in the context of the chosen multi-band multi-standard, radio architecture and the adopted

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This paper discusses various implementations of wide range frequency tuning for LC-VCOs in CMOS processes. It is shown that wide tuning ranges are required in certain applications to compensate for process variation, and in others to allow for wide locking ranges in PLLs. The

Term Paper Wide range tuning in CMOS LC-VCOs

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The article discusses auto calibration of phase lock loops (PLLs), particularly when used as frequency synthesizers in fully integrated radios targeting future generations of broadband wireless applications. These PLLs use wideband voltage-controlled oscillators (VCOs) covering a wide tuning range. A calibration technique is discussed and used in a wireless LAN radio.

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